

IN THE SPECIFICATION:

Please replace the paragraph beginning at page 31, line 17 with the following paragraph:

AI
As described in Embodiment + 2, the loop filter 22 is configured by: the first integration section consisting of the capacitors C1 and C2 and the resistor R1; the voltage level shift section consisting of the resistor R2 and the transistor Tr1; and the second integration section consisting of the resistor R3 and the capacitor C3, and performs smoothing of the signal output from the PLL synthesizer IC 21, and a level shift based on the DC voltage Vb. As the capacitors C1 to C3, capacitors of the ceramic type are used in view of the high-frequency characteristics and size.

Please delete the abstract on page 47 of the specification and replace it with the Abstract of the Disclosure submitted herewith on a separate sheet of paper.